



		Subclass
	Class	ISSUE CLASSIFICATION

**PATENT NUMBER**

**U.S. UTILITY** Patent Application

O.I.P.E. <i>ACJ</i>	PATENT DATE <i>CH</i>
SCANNED	Q.A.

CLASS 09/591621	APPLICATION NO.	CONT/PRIOR	CLASS 703	SUBCLASS 14	ART UNIT 2763 2123	EXAMINER Koselke-Ha Hot
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Vidyabhusan Gupta

API

# System and method for designing and optimizing the memory of an embedded processing system

PTO-2040  
12/99

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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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